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Sheet 1 of 2

Applicant: Roger Hitchcock

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CIRCUIT FOR COMPENSATING RISE TIMES AND RINGING IN HIGH
IMPEDANCE LOADSINFORMATION DISCLOSURE CITATIONS MADE BY APPLICANTU.S. Patent Documents

IDC-US Ref -12

Examiner Initial*	Document Number	Issue Date	Name
<u>MN</u> 1	4,005,318 ⁴	01/25/1977	Zinn
2	4,255,668	03/08/10/1981	Harrison
3	4,272,692	06/09/1981	Morse
4	4,275,317	06/23/1981	Laundenslager, et al.
5	4,442,362	04/10/1984	Rao
6	4,684,820	08/04/1987	Valencia
7	5,089,727	02/18/1992	Molitor, et al.
8	5,495,209	02/29/1996	Gerstenberg
9	5,621,255	04/15/1997	Leon, et al.
10	5,900,681	05/04/1999	Johansson, et al.
11	6,355,992	03/12/2002	Via
<u>MN</u> 12	6,633,093	10/14/2003	Rim, et al.

Foreign Patent Documents

IDC-For Ref -3

Examiner Initial*	Document Number	Publication Date	Country or Patent Office
<u>MN</u> 13	JP2002/000277282	06/22/2001	Japan
<u>MN</u> 14	RU1999/000118119	05/20/2001	Russia
<u>MN</u> 15	WO99/60679	11/25/1999	PCT

Examiner: Matthew V. Nguyen Date Considered: 2/9/06

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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